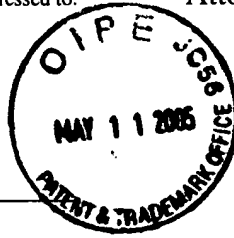


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PATENT  
Attorney Docket No.: 022193-010210US  
Client Reference No.: US 09/828600  
(URI-164)



On

5/9/05

TOWNSEND and TOWNSEND and CREW LLP

By:

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Augustus K. Uht et al.

Application No.: 09/828,600

Filed: April 6, 2001

For: RESOURCE FLOW COMPUTING  
DEVICE

Examiner: Richard L. Ellis

Art Unit: 2183

SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT UNDER 37  
CFR §1.97 and §1.98

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

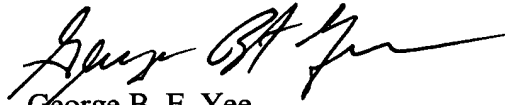
As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



George B. F. Yee  
Reg. No. 37,478

TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, Eighth Floor  
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60446489 v1



Substitute for form 1449A/PTO

*(use as many sheets as necessary)*

**Complete if Known**

Application Number	09/828,600
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First Named Inventor	Uht, Augustus K.
Art Unit	2183
Examiner Name	Richard L. Ellis
Attorney Docket Number	022193-010210US

Sheet	1	of	8
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## U.S. PATENT DOCUMENTS+

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## FOREIGN PATENT DOCUMENTS

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			Application Number	09/828,600	
			Filing Date	April 6, 2001	
			First Named Inventor	Uht, Augustus K.	
			Art Unit	2183	
			Examiner Name	Richard L. Ellis	
Sheet	2	of	8	Attorney Docket Number	022193-010210US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	Agerwala et al., "Data Flow Systems - Special Issue," <i>IEEE COMPUTER</i> , vol. 15, no. 2, pp. 10-13, 1982.	
	2	Aiken et al., "Perfect Pipelining: A New Loop Parallelization Technique," in <i>Proceedings of the 1988 European Symposium on Programming</i> , 1988, 15 pages total.	
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	9	Calder et al., "Value profiling," in <i>Proceedings of the 30th IEEE Symposium on Microarchitecture</i> , December 1997.	
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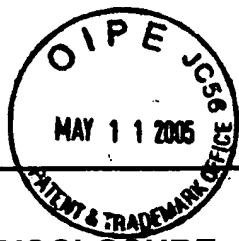
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**INFORMATION DISCLOSURE  
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Sheet 4 of 8

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Application Number	09/828,600
Filing Date	April 6, 2001
First Named Inventor	Uht, Augustus K.
Art Unit	2183
Examiner Name	Richard L. Ellis
Attorney Docket Number	022193-010210US

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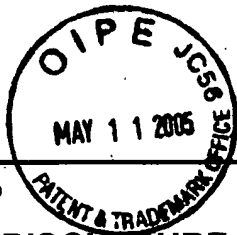
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			First Named Inventor	Uht, Augustus K.	
			Art Unit	2183	
			Examiner Name	Richard L. Ellis	
Sheet	5	of	8	Attorney Docket Number	022193-010210US

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	47	Lilja, "Reducing the Branch Penalty in Pipelined Processors," <i>COMPUTER</i> , vol. 21, pp. 47-55, July 1988.	
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Sheet **6** of **8****Complete if Known**

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First Named Inventor	Uht, Augustus K.
Art Unit	2183
Examiner Name	Richard L. Ellis
Attorney Docket Number	022193-010210US

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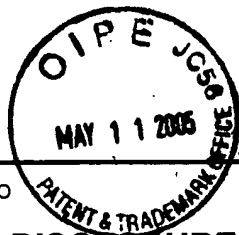
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	61	Popescu et al., "The Metaflow Architecture," <i>IEEE MICRO</i> , vol. 11, no. 3, June 1991, pp. 10-13 & 63-73.	
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			Filing Date	April 6, 2001	
			First Named Inventor	Uht, Augustus K.	
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Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	77	Su et al., "GURPR - A Method for Global Software Pipelining," in <i>Proceedings of the Twentieth Annual Workshop on Microprogramming (MICRO-20)</i> , Association of Computing Machinery, pp.88-96, December 1987.	
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Examiner Signature		Date Considered	
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<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>2</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.



<b>Substitute for form 1449B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	09/828,600	
			Filing Date	April 6, 2001	
			First Named Inventor	Uht, Augustus K.	
			Art Unit	2183	
			Examiner Name	Richard L. Ellis	
Sheet	8	of	8	Attorney Docket Number	022193-010210US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	92	Uht, "High Performance Memory System for High ILP Microarchitectures," Technical Report 0797-0002, Department of Electrical and Computer Engineering, University of Rhode Island, August 26, 1997. Available via <a href="http://ele.uri.edu/~uht">http://ele.uri.edu/~uht</a> , 10 pages total.	
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